LECTURE-2

LOGIC GATES

Logic gates perform basic logical functions and are the fundamental building blocks of digital integrated circuits

By combining thousands or millions of logic gates, it is possible to perform highly complex operations.

In the following examples, each logic gate except the NOT gate has two inputs, A and B, which can either be 1 (True) or 0 (False). The resulting output is a single value of 1 if the result is true, or 0 if the result is false.

- **1 AND** True if A and B are both True
- 2 OR True if either A or B are True
- **3** NOT Inverts value: True if input is False; False if input is True
- 4 XOR True if either A or B are True, but False if both are True
- **5** NAND AND followed by NOT: False only if A and B are both True
- **6** NOR OR followed by NOT: True only if A and B are both False
- 7 **XNOR** XOR followed by NOT: True if A and B are both True or both False

Among all the gates, AND, OR & NOT gate are **Basic gates**, because these three gates have basic operation. NOR & NAND gates are known as universal gates. They are simply a NOT gate added AND & OR gate. And they are known as **universal gates** because all the rest of 6 gates can be made by only NAND gate or only NOR gate.

Name	Graphic symbol	Algebraic function	Truth table
AND	x y F	$F = x \cdot y$	x y F 0 0 0 0 1 0 1 0 0 1 1 1
OR		F = x + y	$\begin{array}{c ccc} x & y & F \\ \hline 0 & 0 & 0 \\ 0 & 1 & 1 \\ 1 & 0 & 1 \\ 1 & 1 & 1 \end{array}$
Inverter	xF	$F = x^{*}$	x F 0 1 1 0
Buffer	xF	F = x	x F 0 0 1 1
NAND		F = (xy)'	x y F 0 0 1 0 1 1 1 0 1 1 1 0
NOR	x y F	$F = (x + y)^{\prime}$	x y F 0 0 1 0 1 0 1 0 0 1 1 0
Exclusive-OR (XOR)		$F = xy' + x'y$ $= x \oplus y$	x y F 0 0 0 0 1 1 1 0 1 1 1 0
Exclusive-NOR or equivalence		$F = xy + x'y'$ $= (x \oplus y)'$	x y F 0 0 1 0 1 0 1 0 0 1 1 1